

(19)



JAPANESE PATENT OFFICE

PATENT ABSTRACTS OF JAPAN

Ref. no. 1

(11) Publication number: **61058037 A**

(43) Date of publication of application: **25 . 03 . 86**

(51) Int. Cl. **G06F 9/00**  
**G06F 9/46**  
**G06F 13/24**

(21) Application number: **59152367**

(71) Applicant: **FUJITSU LTD**

(22) Date of filing: **23 . 07 . 84**

(72) Inventor: **NOJIMA KENICHI**

(54) **INTERRUPTION CONTROL SYSTEM TO SERVICE PROCESSOR** COPYRIGHT: (C)1986,JPO&Japio

(57) Abstract:

PURPOSE: To decrease the number of signal lines by providing an interruption register on which interruptin cause is displayed on each main body and which is read from a service process (SVP) 3 and a mask register written from the SVP and using an output of bothe registers as an interruption signal specifically.

CONSTITUTION: In controlling a main body device 1 by the SVP 3 via an interface coantroller 2, the interruption register 11 on which an interruption cause is displayed on each device and which is read from the SVP 3 and the mask register 12 masking the interruption at each interruption cause and writt/en from the SVP 3 are provided to the device 1. Further, a signal of OR outputs of ANDed outputs at each bit from the registers 11, 12 is informed to the controller 2 as an interruption signal, the SVP 3 references an interruption register 25 and a mask register 26 in the controller 2, the device 1 being a cause to interruption is recognized and the interruption cause is recognized by referencing the register 11, 12 of the device 1.

